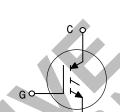
Designer's™ Data Sheet

Insulated Gate Bipolar Transistor

N-Channel Enhancement-Mode Silicon Gate

This Insulated Gate Bipolar Transistor (IGBT) uses an advanced termination scheme to provide an enhanced and reliable high voltage blocking capability. Short circuit rated IGBT's are specifically suited for applications requiring a guaranteed short circuit withstand time. Fast switching characteristics result in efficient operation at high frequencies.

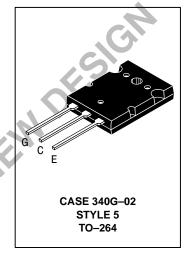
- Industry Standard High Power TO-264 Package (TO-3PBL)
- High Speed E_{off}: 216 μJ/A typical at 125°C
- High Short Circuit Capability 10 μs minimum
- Robust High Voltage Termination



MGY25N120

Motorola Preferred Device

IGBT IN TO-264
25 A @ 90°C
38 A @ 25°C
1200 VOLTS
SHORT CIRCUIT RATED



MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	V _{CES}	1200	Vdc	
Collector–Gate Voltage (R _{GE} = 1.0 MΩ)	V _{CGR}	1200	Vdc	
Gate-Emitter Voltage — Continuous	V _{GE}	±20	Vdc	
Collector Current — Continuous @ T _C = 25°C — Continuous @ T _C = 90°C — Repetitive Pulsed Current (1)	I _{C25} I _{C90} I _{CM}	38 25 76	Adc Apk	
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	212 1.69	Watts W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to 150	°C	
Short Circuit Withstand Time (V_{CC} = 720 Vdc, V_{GE} = 15 Vdc, T_J = 125°C, R_G = 20 Ω)	t _{sc}	10	μs	
Thermal Resistance — Junction to Case – IGBT — Junction to Ambient	R _{θJC} R _{θJA}	0.6 35	°C/W	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	TL	260	°C	
Mounting Torque, 6–32 or M3 screw	10	10 lbf•in (1.13 N•m)		

⁽¹⁾ Pulse width is limited by maximum junction temperature. Repetitive rating.

Designer's Data for "Worst Case" Conditions — The Designer's Data Sheet permits the design of most circuits entirely from the information presented. SOA Limit curves — representing boundaries on device characteristics — are given to facilitate "worst case" design.

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Preferred devices are Motorola recommended choices for future use and best overall value.

REV₃



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ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

C	naracteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–to–Emitter Breakdown ($V_{GE} = 0$ Vdc, $I_{C} = 25 \mu$ Adc) Temperature Coefficient (Posit		V _{(BR)CES}	1200 —	— 960	_	Vdc mV/°C
Emitter–to–Collector Breakdown Voltage (V _{GE} = 0 Vdc, I _{EC} = 100 mAdc)		V _{(BR)ECS}	25	_	_	Vdc
Zero Gate Voltage Collector Curr ($V_{CE} = 1200 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}$ ($V_{CE} = 1200 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}$)	ICES			100 2500	μAdc
Gate-Body Leakage Current (V _G	_E = ± 20 Vdc, V _{CE} = 0 Vdc)	I _{GES}	_	_	250	nAdc
ON CHARACTERISTICS (1)						_
Collector-to-Emitter On-State V (V_{GE} = 15 Vdc, I_{C} = 12.5 Adc) (V_{GE} = 15 Vdc, I_{C} = 12.5 Adc, (V_{GE} = 15 Vdc, I_{C} = 25 Adc)		V _{CE(on)}		2.37 2.15 2.98	3.24	Vdc
Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 1.0 mAdc) Threshold Temperature Coeffic	ient (Negative)	V _{GE(th)}	4.0 —	6.0 10	8.0 —	Vdc mV/°C
Forward Transconductance (V _{CE}	= 10 Vdc, I _C = 25 Adc)	9 _{fe}		12	_	Mhos
DYNAMIC CHARACTERISTICS				7		
Input Capacitance	0, 05,41,4, 0,41,4	C _{ies}		2795	_	pF
Output Capacitance	$(V_{CE} = 25 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oes}		181	_	
Transfer Capacitance		C _{res}	_	45	_	
SWITCHING CHARACTERISTICS	5 (1)	7,0				
Turn-On Delay Time		t _{d(on)}	_	91	_	ns
Rise Time	$(V_{CC} = 720 \text{ Vdc}, I_{C} = 25 \text{ Adc},$	t _r	_	124	_	
Turn-Off Delay Time	$V_{GE} = 15 \text{ Vdc}, L = 300 \mu H$ $R_{G} = 20 \Omega$	t _{d(off)}	_	196	_	
Fall Time	Energy losses include "tail"	t _f	_	310	_	
Turn-Off Switching Loss		E _{off}	_	2.44	4.69	mJ
Turn-On Delay Time		t _{d(on)}	_	88	_	ns
Rise Time	(V _{CC} = 720 Vdc, I _C = 25 Adc,	t _r	_	126	_	
Turn-Off Delay Time	$V_{GE} = 15 \text{ Vdc}, L = 300 \mu H$ $R_{G} = 20 \Omega, T_{J} = 125^{\circ}C)$	t _{d(off)}	_	236	_	
Fall Time	Energy losses include "tail"	t _f	_	640	_	
Turn-Off Switching Loss		E _{off}	_	5.40	_	mJ
Gate Charge		Q _T	_	97	_	nC
	$(V_{CC} = 720 \text{ Vdc}, I_{C} = 25 \text{ Adc}, V_{GF} = 15 \text{ Vdc})$	Q ₁	_	31	_	
	vGE = 13 vuc)	Q ₂	_	40	_	
NTERNAL PACKAGE INDUCTAN	ICE					
Internal Emitter Inductance (Measured from the emitter lea	d 0.25" from package to emitter bond pad)	LE	_	13	_	nH

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

TYPICAL ELECTRICAL CHARACTERISTICS

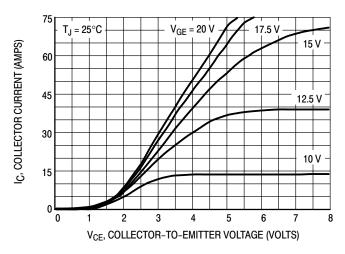


Figure 1. Output Characteristics

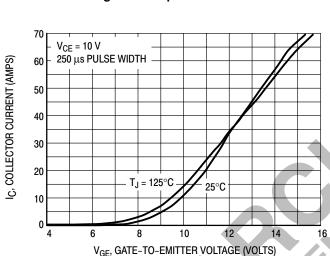


Figure 3. Transfer Characteristics

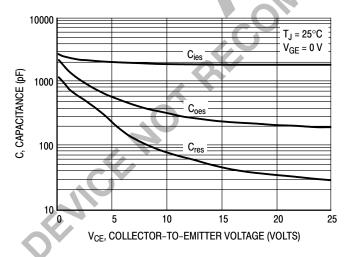


Figure 5. Capacitance Variation

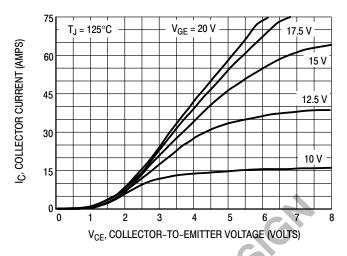


Figure 2. Output Characteristics

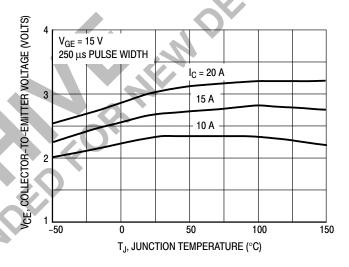


Figure 4. Collector-to-Emitter Saturation Voltage versus Junction Temperature

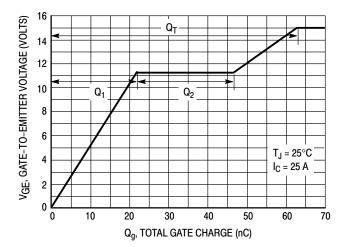
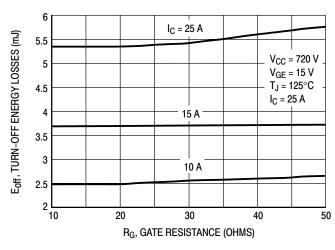


Figure 6. Gate-to-Emitter Voltage versus
Total Charge

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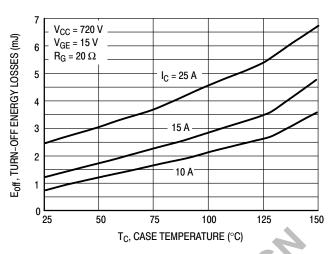


Figure 8. Turn-Off Losses versus

Case Temperature

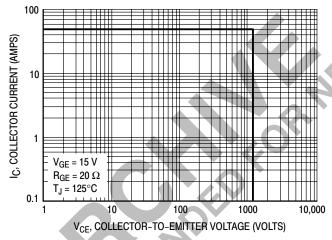


Figure 9. Reverse Biased Safe Operating Area

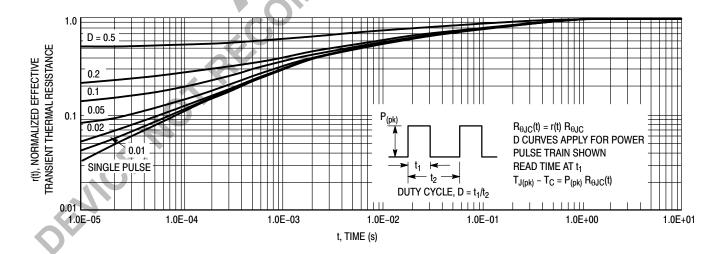
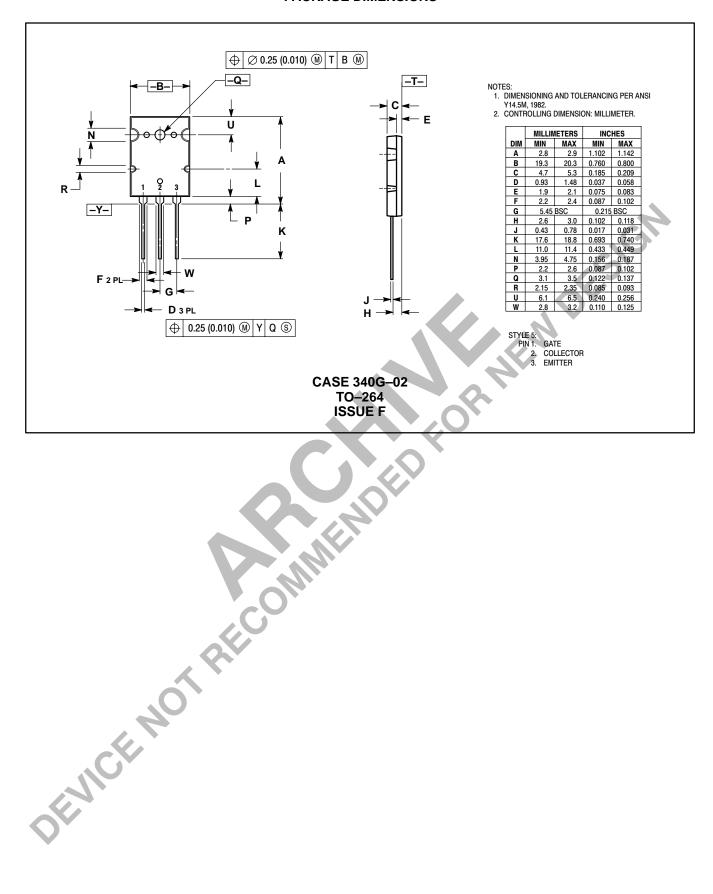


Figure 10. Thermal Response

PACKAGE DIMENSIONS





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